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PTO/SB/31 (04-05)
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NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES			Docket Number (Optional) P-8716-US		
	in re Application of SHAPPIR, Asset et al.				
	Application Number 10/6			Filed April 03, 2005	
	METHOD, CIRCUIT AND SYSTEMS FOR ERASING ONE OR MORE FOR NON-VOLATILE MEMORY CELLS				
	Group Art Unit 2824 Examiner NGUYEN, Dang T.				
Applicant hereby appeals to the Board of Patent Appeals and interferences from the last decision of the Examinar					
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